

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2256	memory near4 (adapter or interface) near4 DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:38
L2	721	host near4 (adapter or interface) near4 DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:40
L3	782	(processor or CPU or microprocessor) near4 (adapter or interface) near4 DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:40
L4	55	L1 and L2 and L3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:28
L5	21	L1 with (disk near3 controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:28
L6	1589	709/212,213.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:29
L7	2104	710/22,308,316.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:29
L8	12419	370/351-360,362-364,368-374.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:29
L9	9135	370/401,402,532-541.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:29

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L10	125	710/51.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:29
L11	15341	(memory storage buffer fifo queue) near4 (adapter interface connector controller port bridge) near4 (DMA or (direct near2 memory near2 access))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:42
L12	2459	(pc computer client server host) near4 (adapter interface connector controller port bridge) near4 (DMA or (direct near2 memory near2 access))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:40
L13	5173	(processor or CPU or microprocessor) near4 (adapter interface connector controller port bridge) near4 (DMA or (direct near2 memory near2 access))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:42
L14	677	11 and 12 and 13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:41
L15	19	6 and 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:18
L16	189	7 and 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:41
L17	7	8 and 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:04
L18	14	9 and 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:00
L19	2	10 and 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 12:48

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L20	2	("7120739").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/18 12:48
L21	2884	(channel link path line lane) near4 (adapter interface connector controller port bridge) near4 (DMA or (direct near2 memory near2 access))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:41
L22	2886	(channel link path line lane) near4 (fabric adapter interface connector controller port bridge) near4 (DMA or (direct near2 memory near2 access))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:42
L23	35072	(memory storage buffer fifo queue) near4 (DMA or (direct near2 memory near2 access))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:42
L24	7458	(channel link path line lane) near4 (DMA or (direct near2 memory near2 access))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:42
L25	11820	(processor or CPU or microprocessor microcontroller MPU) near4 (DMA or (direct near2 memory near2 access))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:43
L26	1442	(switch\$4 mux multiplexer cross\$bar) near4 (DMA or (direct near2 memory near2 access))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:43
L27	410	23 and 24 and 25 and 26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:51
L28	170	distributed near3 ((direct near memory near access) or DMA or DMAC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:50
L29	9	27 and 28	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/18 13:52